

**Amendments to the Abstract**

Please add the following Abstract to this application.

-- An apparatus (100) performs a data caching operation responsive to a channel change event which reduces channel change times. According to an exemplary embodiment, the apparatus (100) includes a cache memory (103) operative to cache an incoming data stream responsive to a channel change command. A processor (104) is operative to find program specific information (PSI) included within the incoming data stream. A decoder (104) is operative to decode the cached data stream responsive to the processor (104) finding the program specific information.--